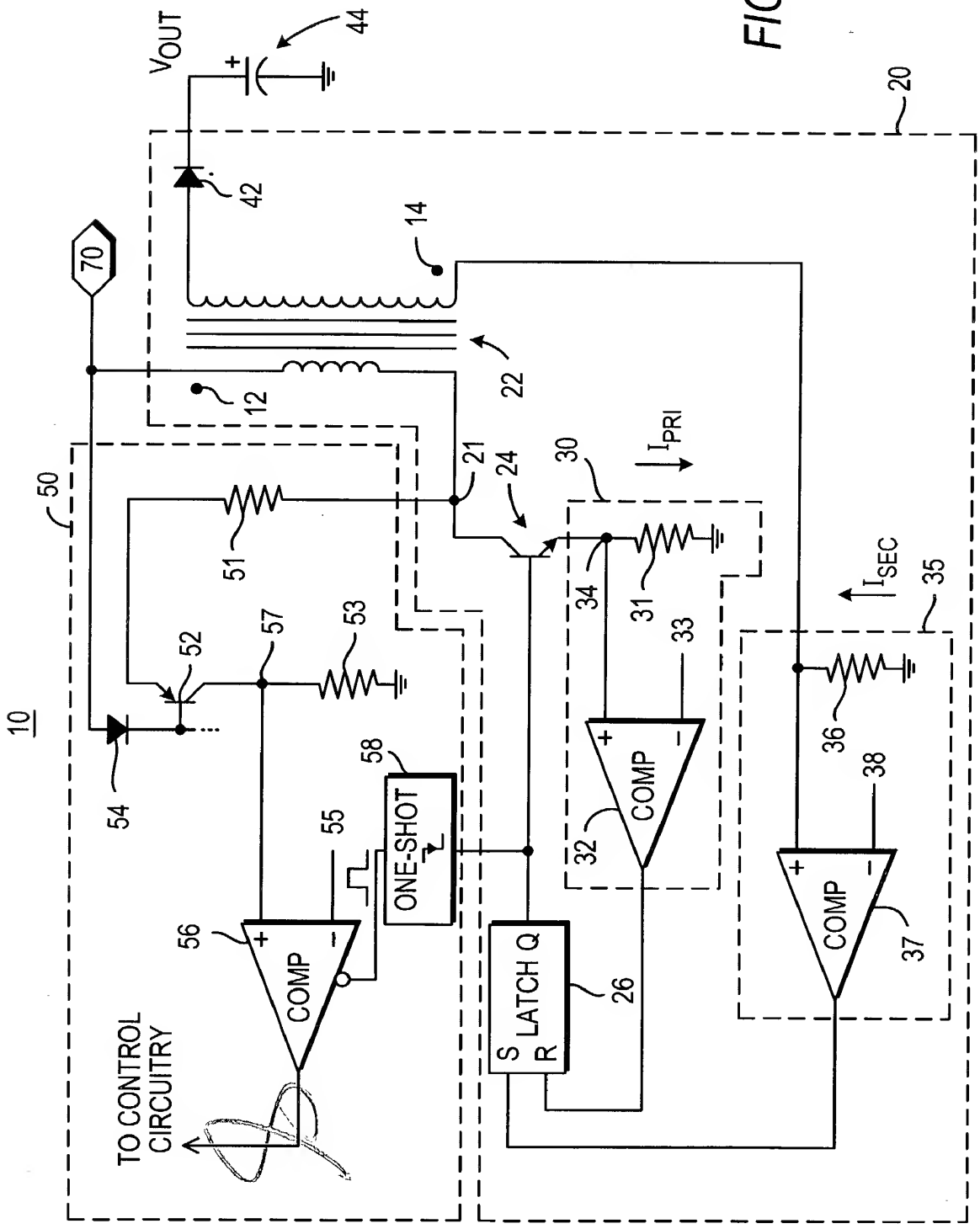
6518733

FIG. 1



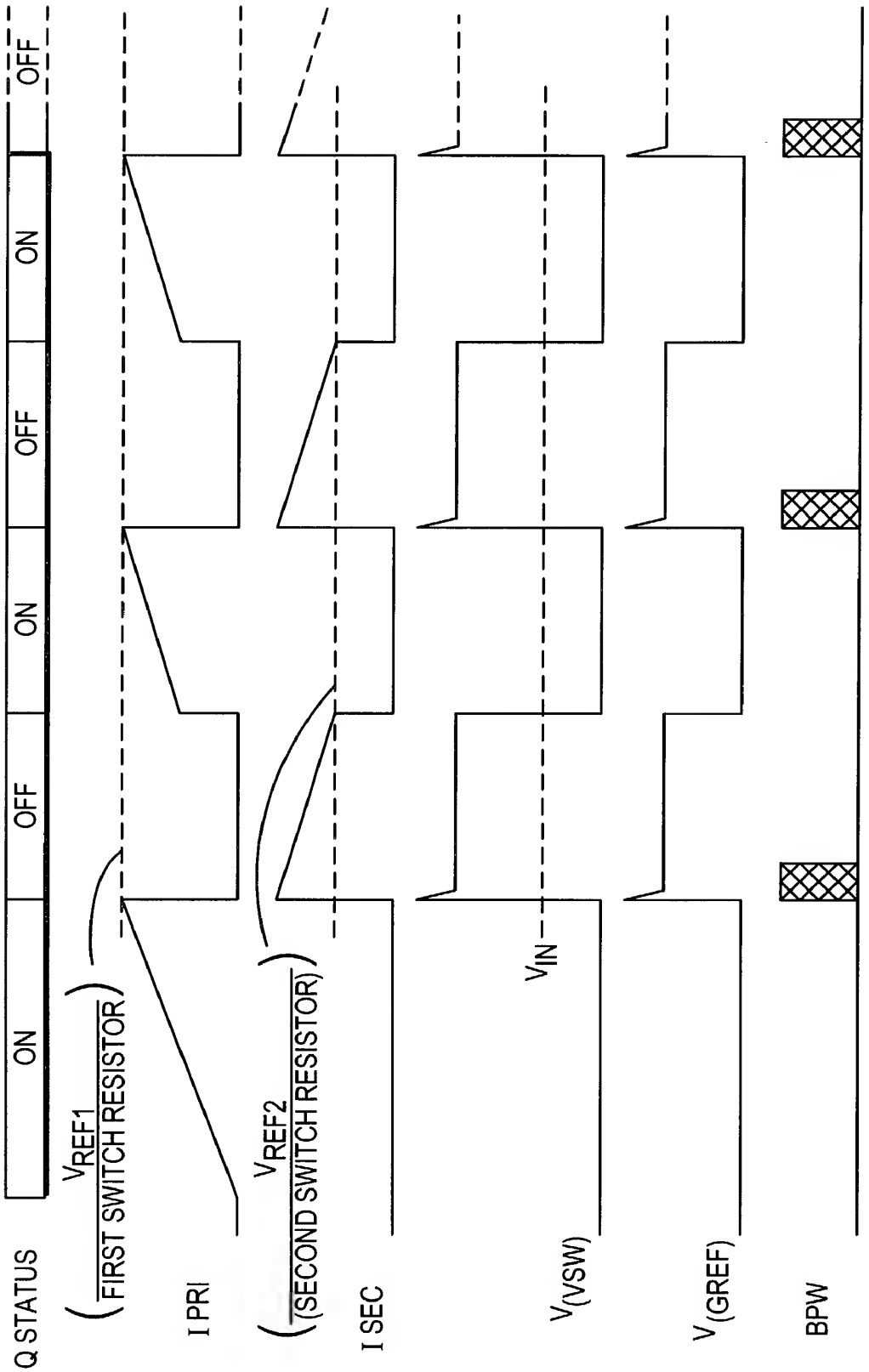

Q STATUS
ON
OFF
ON
OFF
ON
OFF
$V_{REF1}$ / (FIRST SWITCH RESISTOR)
I PRI
$V_{REF2}$ / (SECOND SWITCH RESISTOR)
I SEC
$V_{IN}$
$V_{(VSW)}$
$V_{(GREF)}$
BPW

FIG. 2

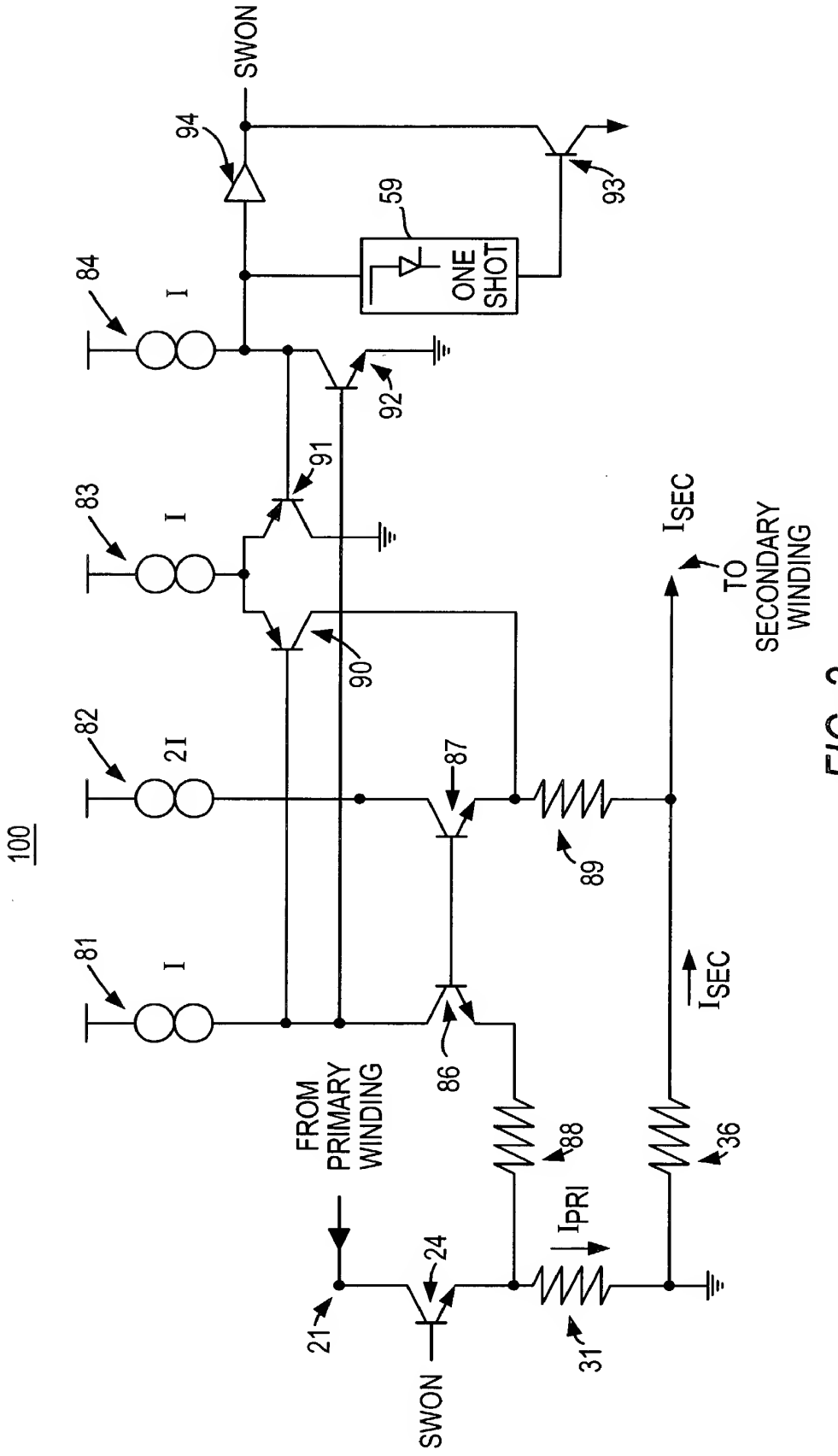

100
SWON
94
93
59
ONE SHOT
84
I
92
91
83
I
90
82
2I
87
89
81
I
86
88
36
24
31
21
SWON
FROM PRIMARY WINDING
IPRI
ISEC
ISEC
TO SECONDARY WINDING

FIG. 3

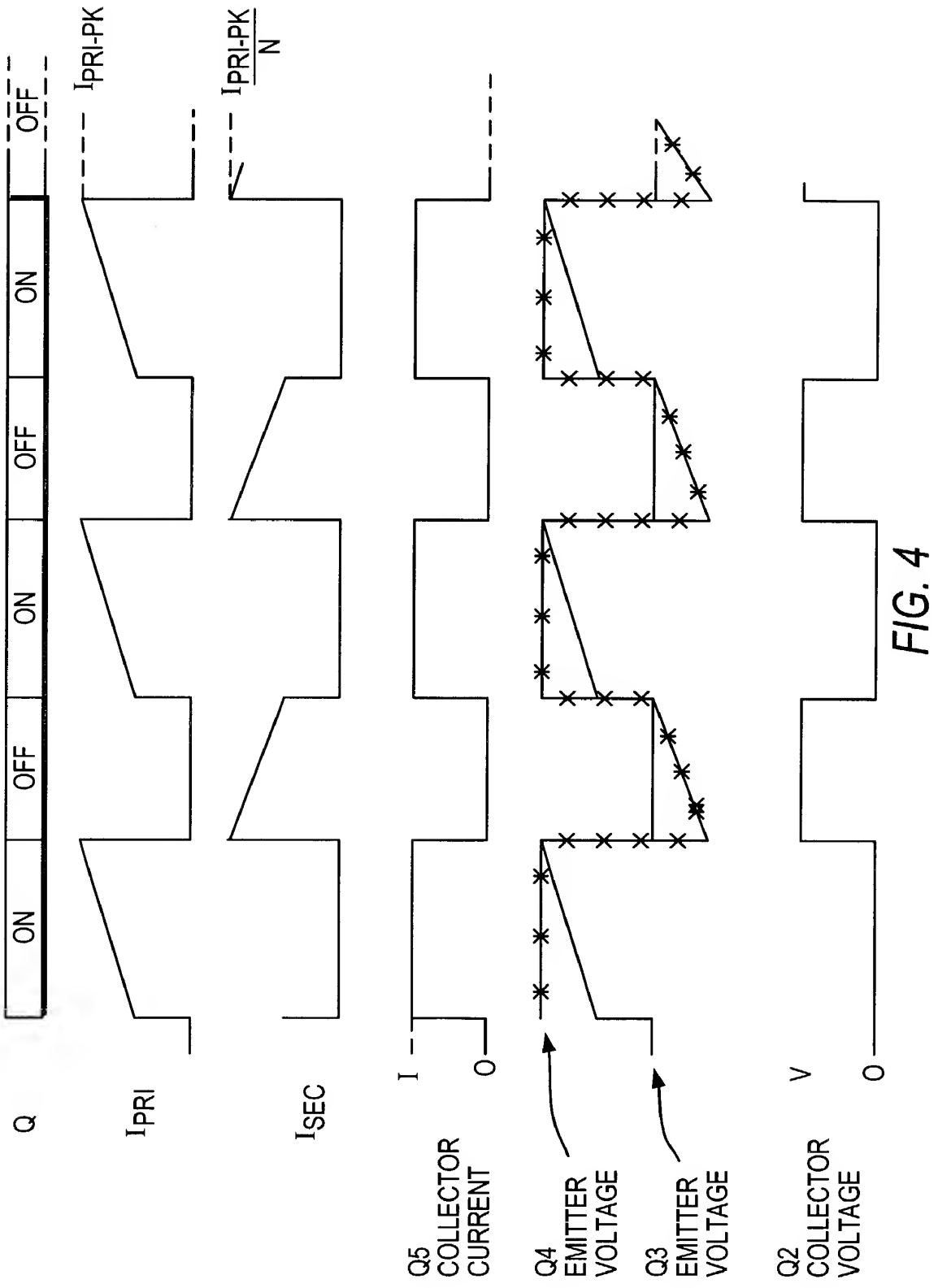

Q
ON
OFF
ON
OFF
ON
OFF
IPRI
IPRI-PK
ISEC
IPRI-PK / N
I
0
Q5 COLLECTOR CURRENT
Q4 EMITTER VOLTAGE
Q3 EMITTER VOLTAGE
V
0
Q2 COLLECTOR VOLTAGE

FIG. 4

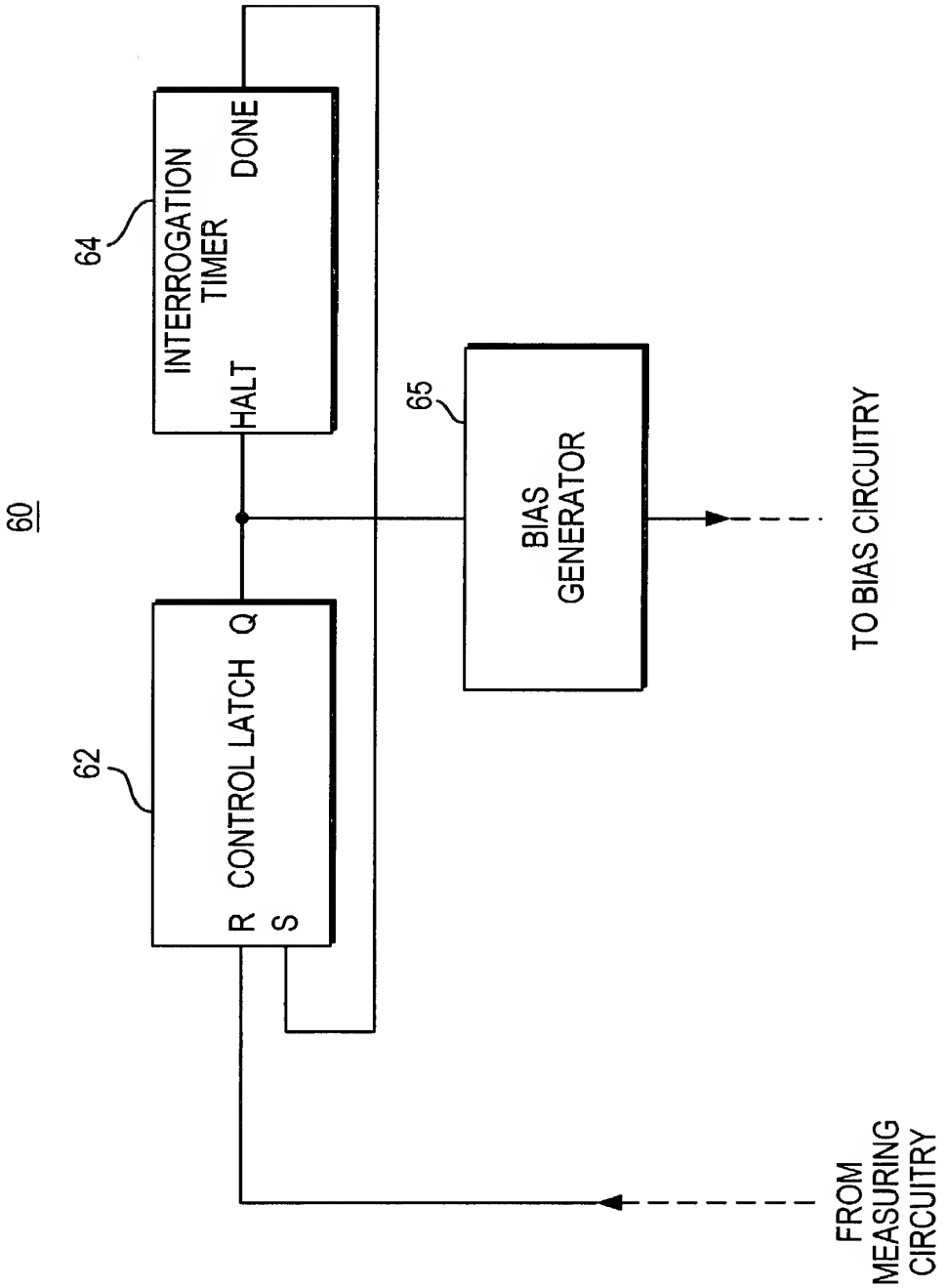

60
64
INTERROGATION TIMER
DONE
HALT
65
BIAS GENERATOR
TO BIAS CIRCUITRY
62
CONTROL LATCH
Q
R
S
FROM MEASURING CIRCUITRY

FIG. 5

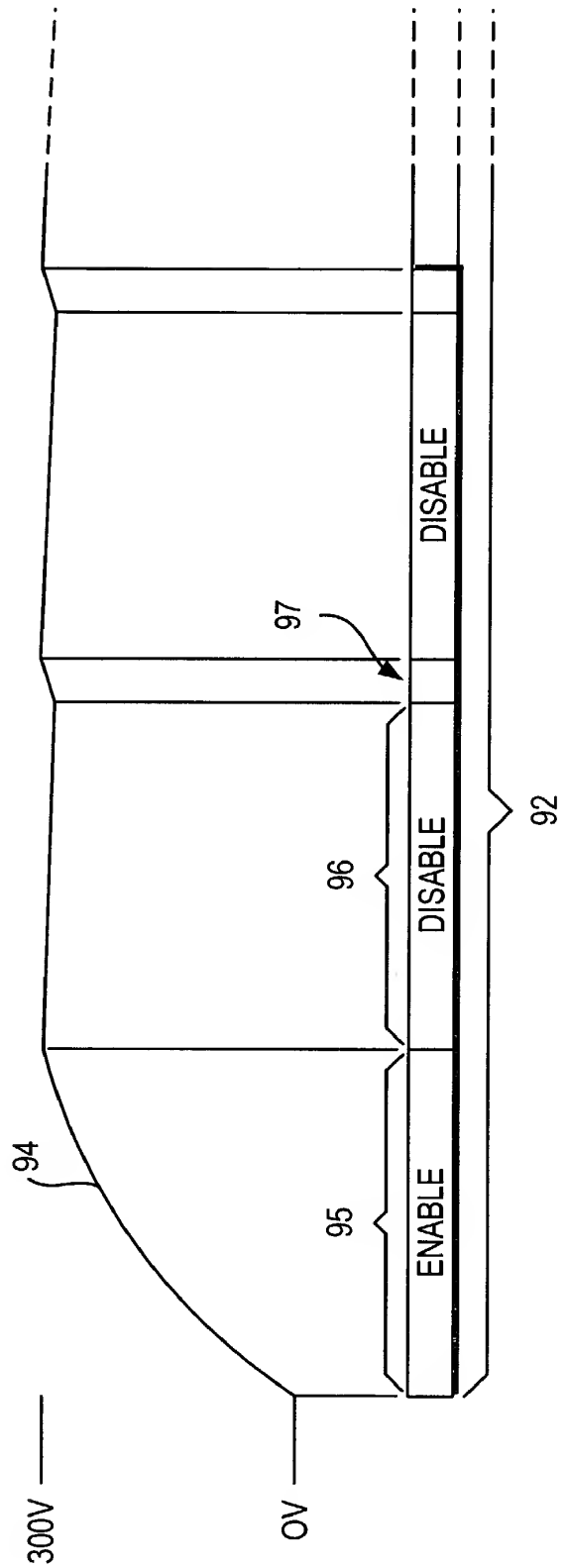



FIG. 6

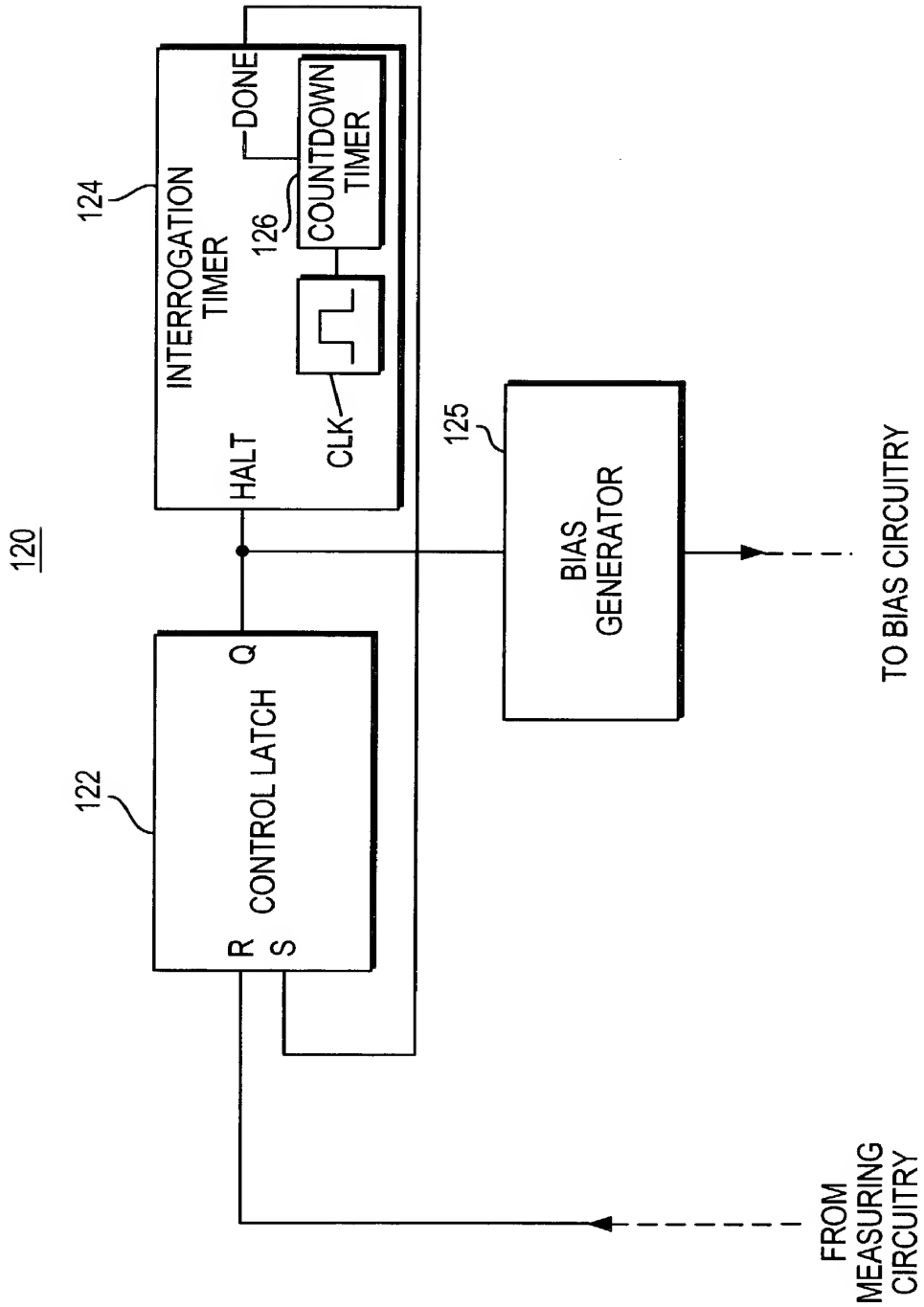

120
124
INTERROGATION TIMER
DONE
126
COUNTDOWN TIMER
CLK
HALT
125
BIAS GENERATOR
TO BIAS CIRCUITRY
122
CONTROL LATCH
Q
R
S
FROM MEASURING CIRCUITRY

FIG. 7

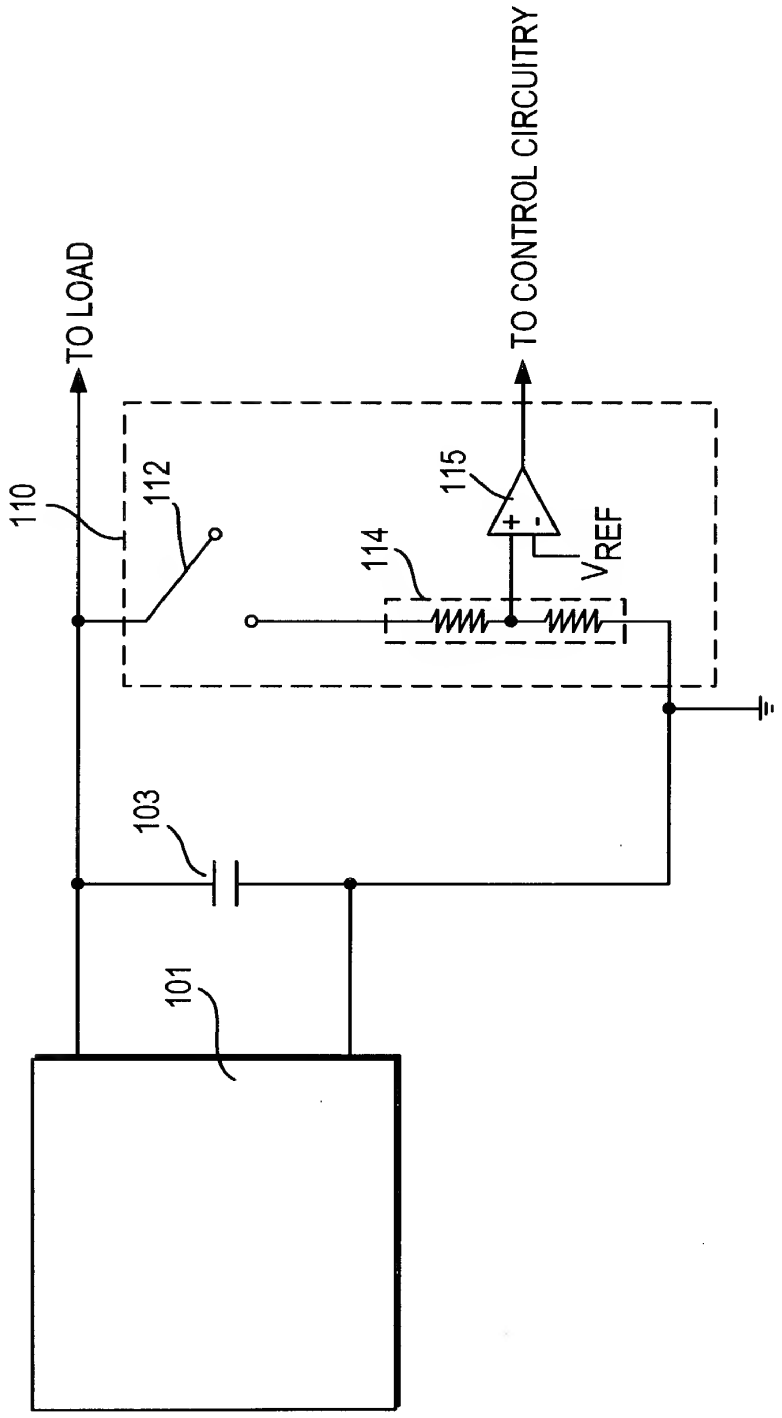



FIG. 8